

FIG. 1A

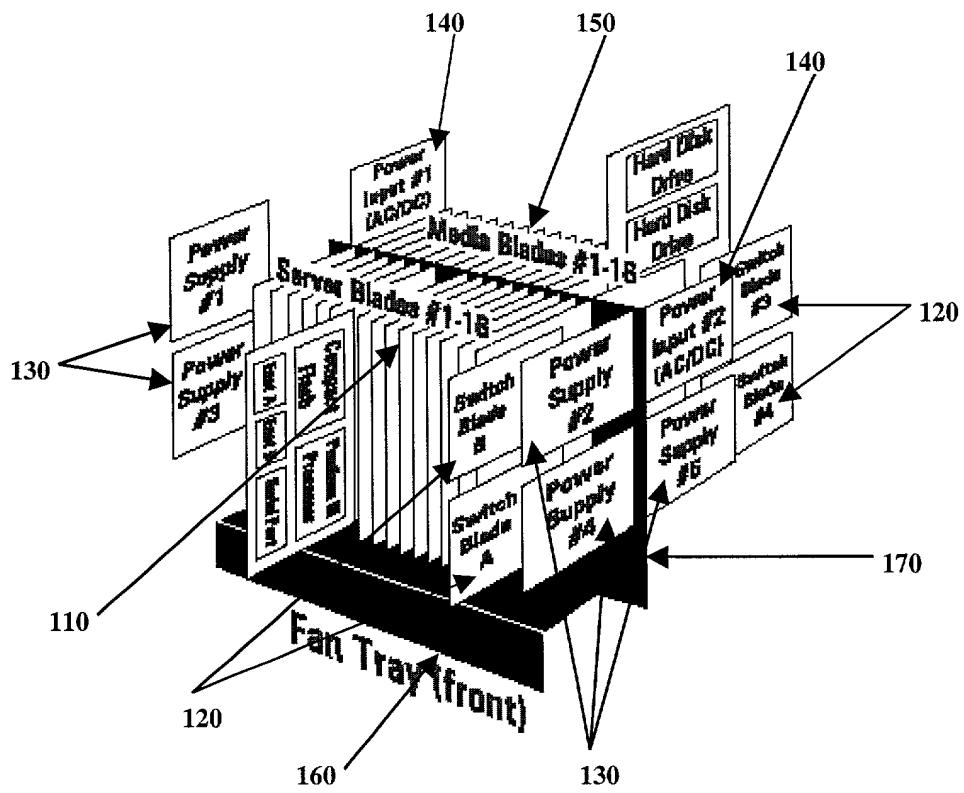


FIG. 1B

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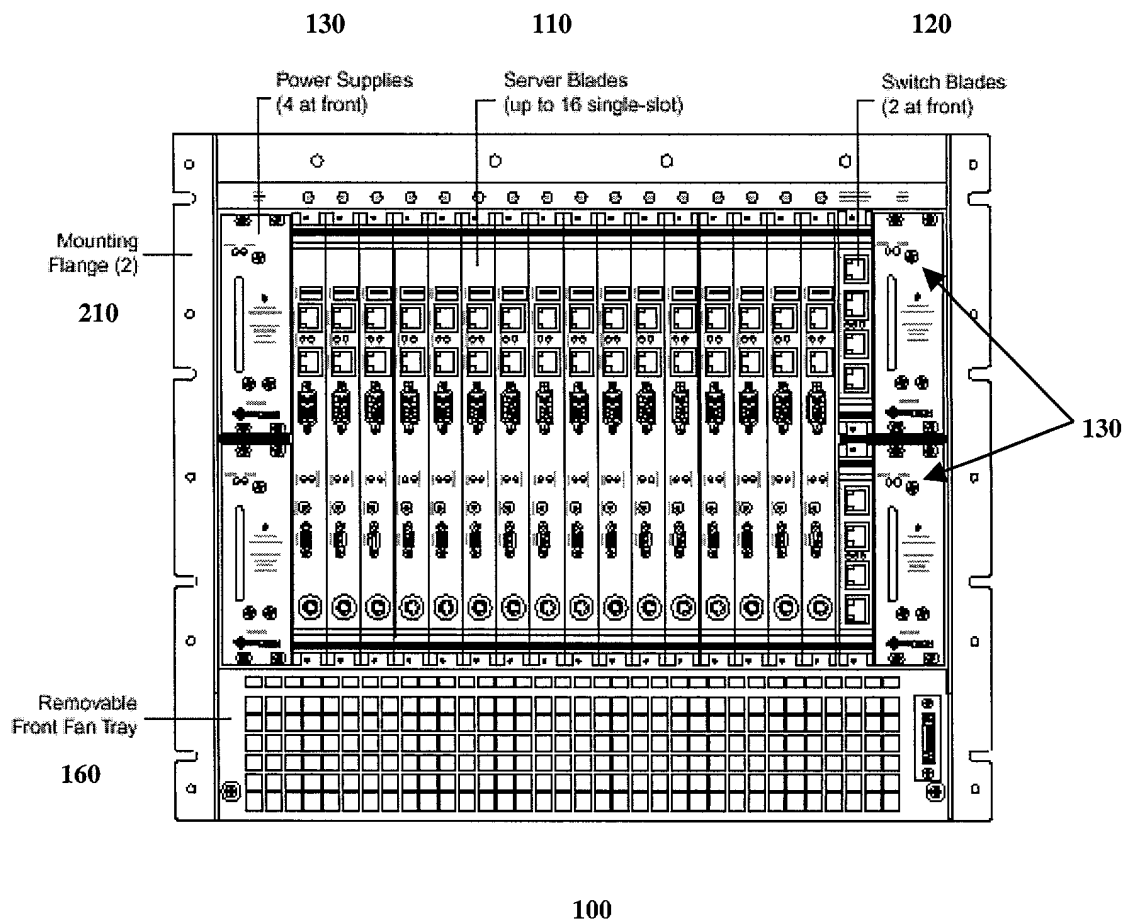


FIG. 2

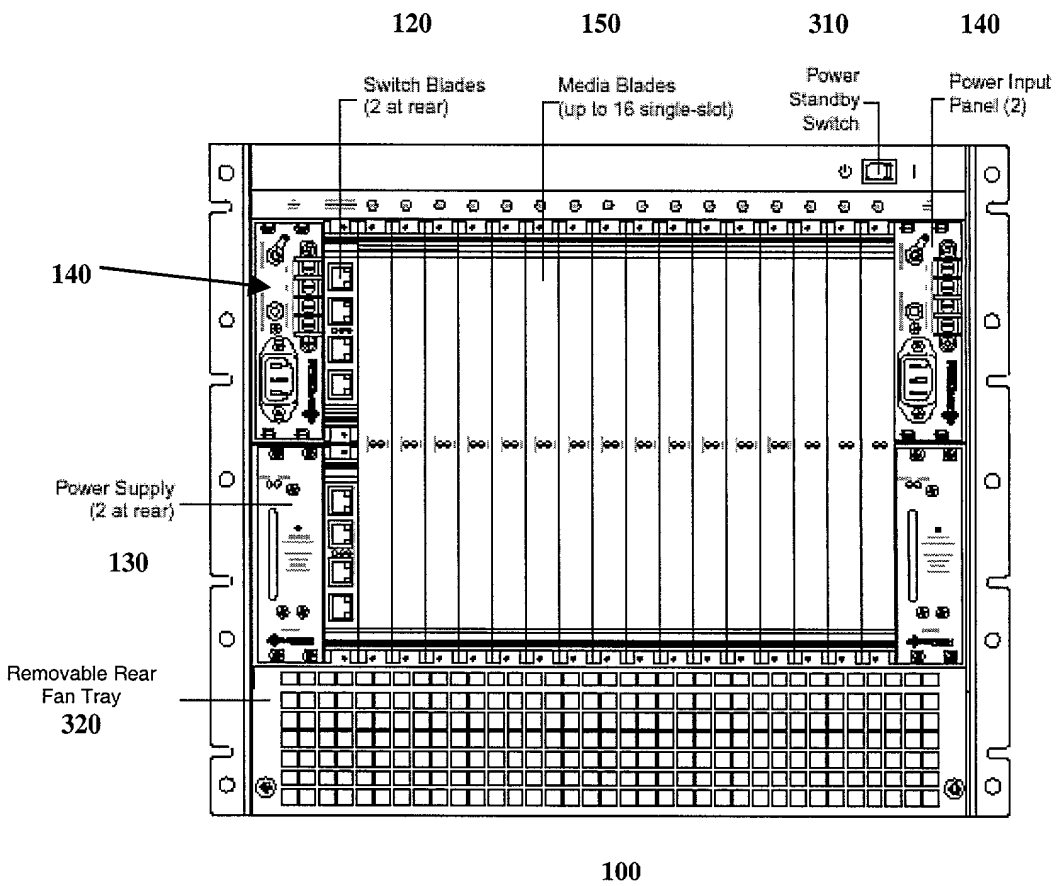


FIG. 3

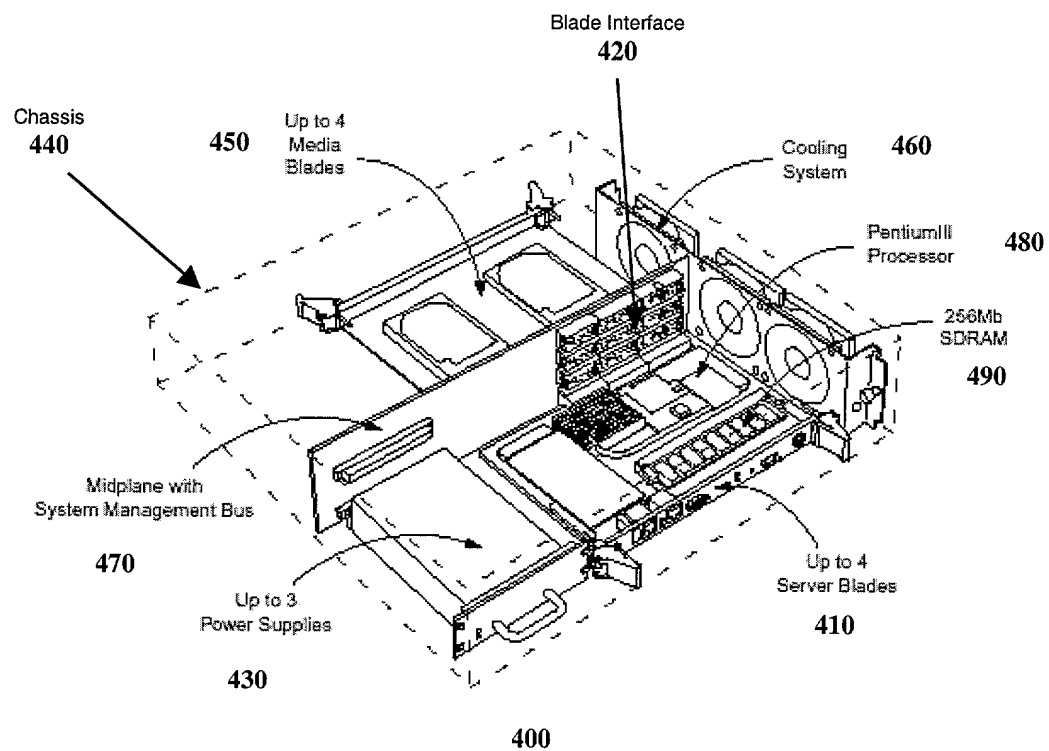


FIG. 4

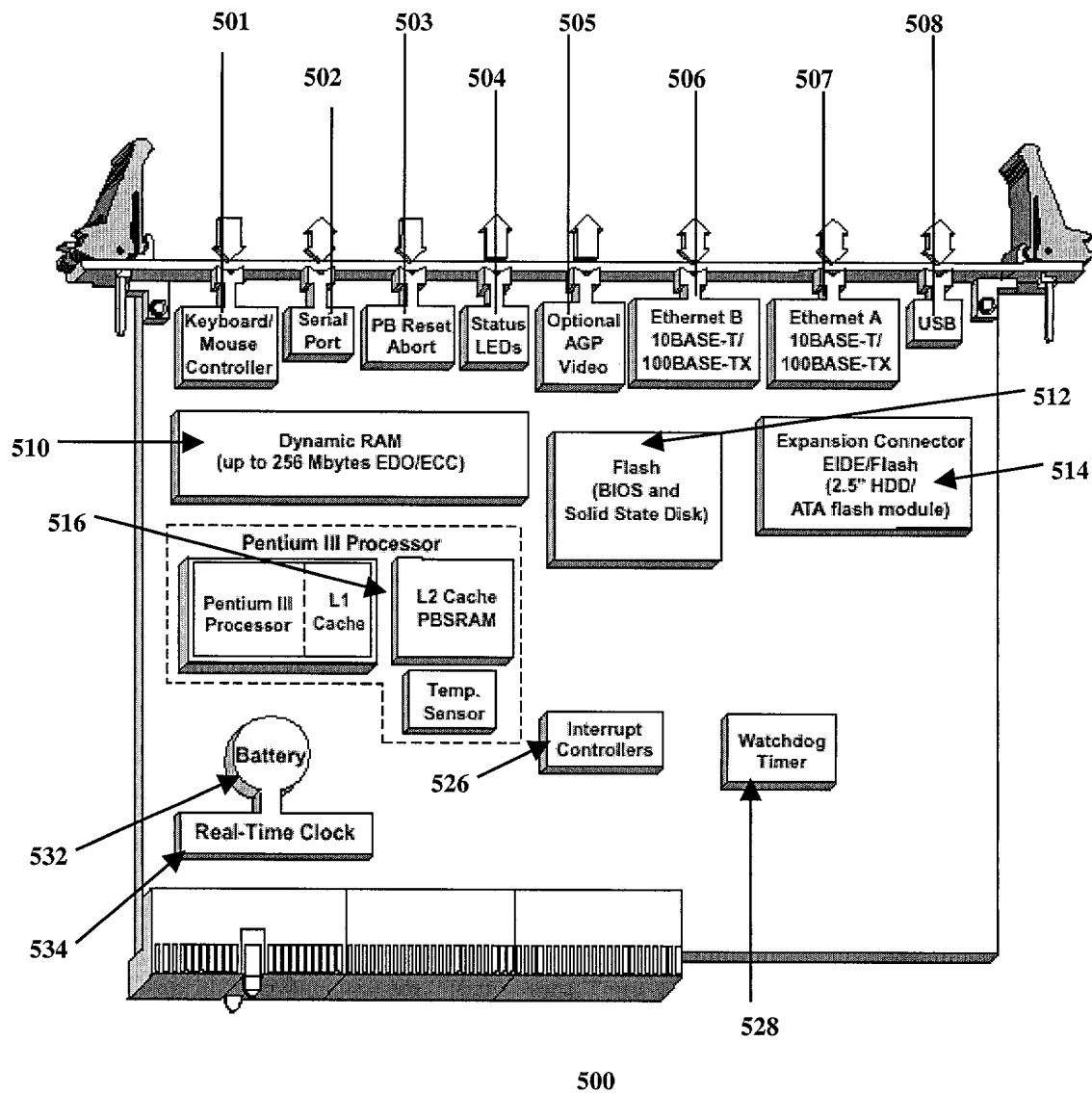


FIG. 5

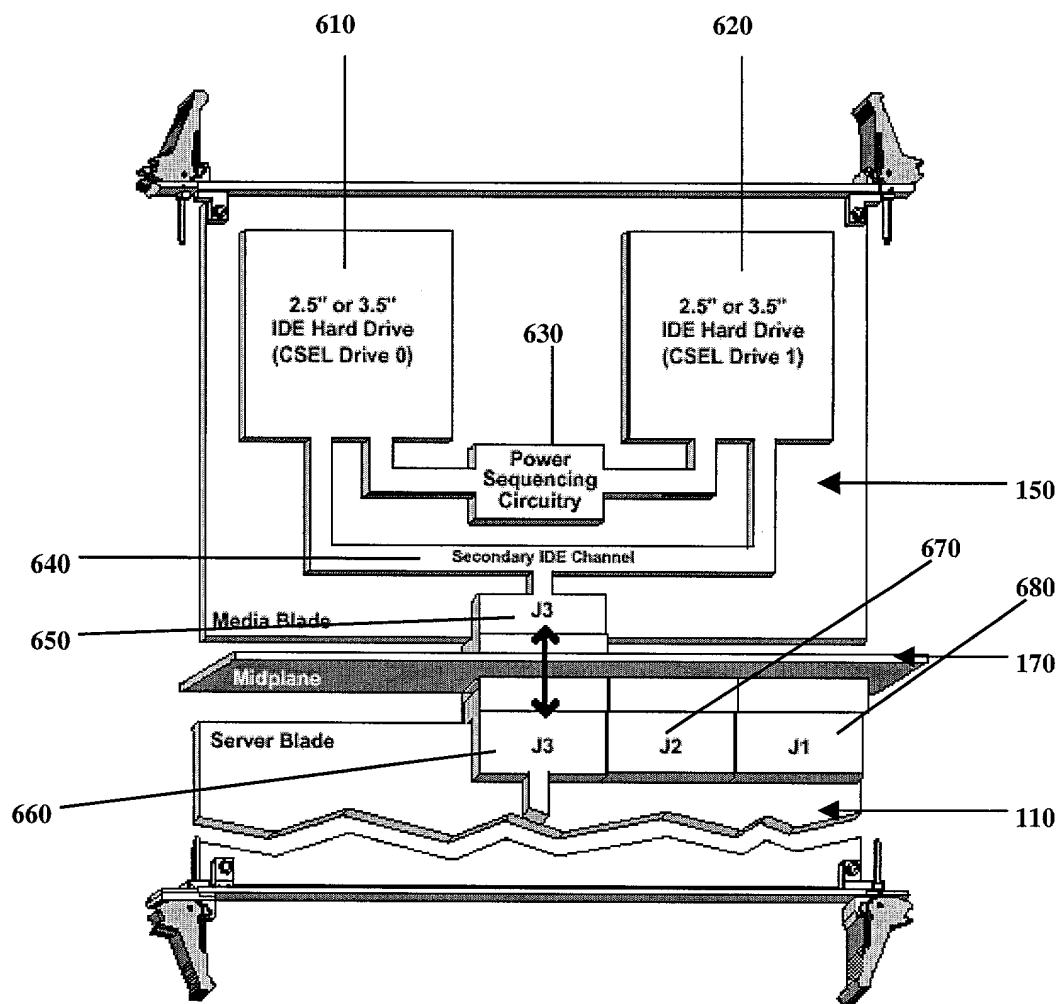


FIG. 6

3U Fabric BOARD, TOP FRONT

J3	11	gnd	VDD -12	SMB SCL	gnd CONTACT#	RXA[0]+	RXA[0]-	gnd	TXA[0]-	TXA[0]+	gnd	J3	11
J3	10	gnd	gnd	SMB WP	gnd	gnd	gnd	gnd	gnd	gnd	gnd	J3	10
J3	9	gnd	VDD +12	SMB SDA	SMB SAL	n/c RES	n/c RES	gnd	TXA[1]-	TXA[1]+	gnd	J3	9
J3	8	gnd	VDD +5	gnd EARTH	n/c RES	n/c QoS	n/c RES	gnd	RXA[1]-	RXA[1]+	gnd	J3	8
J3	7	gnd	VDD +5	VDD IPMB_PWR	n/c GTX_CLK1	n/c RES	n/c	gnd	gnd	gnd	gnd	J3	7
J3	6	gnd	VDD +5	gnd EARTH	gnd	gnd	gnd	gnd	TXA[2]-	TXA[2]+	gnd	J3	6
J3	5	gnd	VDD +3.3	VDD I/O	n/c	n/c	n/c GRX_CLKN1	gnd	RXA[2]-	RXA[2]+	gnd	J3	5
J3	4	gnd	VDD +3.3	gnd EARTH	n/c	gnd	n/c GRX_CLK1	gnd	gnd	gnd	gnd	J3	4
J3	3	gnd	VDD +3.3	VDD I/O	gnd	n/c COM_DET1	gnd	gnd	TXA[3]-	TXA[3]+	gnd	J3	3
J3	2	gnd	VDD +3.3	gnd EARTH	n/c GTXD1[0]	gnd	n/c GRXD1[8]	gnd	RXA[3]-	RXA[3]+	gnd	J3	2
J3	1	gnd	VDD +3.3	VDD I/O	gnd	n/c GTXD1[1]	gnd	gnd	gnd	gnd	gnd	J3	1
J2	25	gnd	VDD +3.3	gnd	n/c GTXD1[2]	gnd	n/c GRXD1[9]	gnd	TXA[4]-	TXA[4]+	gnd	J2	25
J2	24	gnd	VDD +3.3	VDD I/O	gnd	n/c GTXD1[3]	gnd	gnd	RXA[4]-	RXA[4]+	gnd	J2	24
J2	23	gnd	gnd ADDR	gnd	n/c GTXD1[4]	gnd	n/c GRXD1[7]	gnd	gnd	gnd	gnd	J2	23
J2	22	gnd	gnd ADDR	BD_PRES17AF	gnd	n/c GTXD1[5]	gnd	gnd	TXA[5]-	TXA[5]+	gnd	J2	22
J2	21	gnd	ENET_SEL1	gnd	n/c GTXD1[6]	gnd	n/c GRXD1[6]	gnd	RXA[5]-	RXA[5]+	gnd	J2	21
J2	20	gnd	ENET_SEL2	VACANT1A	gnd	n/c GRXD1[5]	gnd	gnd	gnd	gnd	gnd	J2	20
J2	19	gnd	n/c RES	gnd	n/c GTXD1[7]	gnd	n/c GRXD1[4]	gnd	TXA[6]-	TXA[6]+	gnd	J2	19
J2	18	gnd	VDD +5	BD_SEL17AF	gnd	n/c GRXD1[3]	gnd	gnd	RXA[6]-	RXA[6]+	gnd	J2	18
J2	17	gnd	gnd	gnd	n/c GTXD1[9]	gnd	n/c GRXD1[2]	gnd	gnd	gnd	gnd	J2	17
J2	16	gnd	n/c TX[16]+	gnd	gnd	n/c GRXD1[1]	gnd	gnd	TXA[7]-	TXA[7]+	gnd	J2	16
J2	15	gnd	n/c TX[16]-	gnd	n/c GTXD1[8]	gnd	n/c GRXD1[0]	gnd	RXA[7]-	RXA[7]+	gnd	J2	15
J2	14	key	key	key	key	key	key	key	key	key	key	J2	14
J2	13	key	key	key	key	top front	key	key	key	key	key	J2	13
J2	12	key	key	key	key	key	key	key	key	key	key	J2	12
J2	11	gnd	n/c RX[16]-	gnd	n/c GRXD2[9]	gnd	n/c GTXD2[0]	gnd	RXA[8]-	RXA[8]+	gnd	J2	11
J2	10	gnd	n/c RX[16]+	gnd	gnd	n/c GTXD2[1]	gnd	gnd	TXA[8]-	TXA[8]+	gnd	J2	10
J2	9	gnd	gnd	gnd	n/c GRXD2[8]	gnd	n/c GTXD2[2]	gnd	gnd	gnd	gnd	J2	9
J2	8	gnd	VDD +5	BD_SEL17AB	gnd	n/c GTXD2[3]	gnd	gnd	RXA[9]-	RXA[9]+	gnd	J2	8
J2	7	gnd	n/c RES	gnd	n/c GRXD2[7]	gnd	n/c GTXD2[4]	gnd	TXA[9]-	TXA[9]+	gnd	J2	7
J2	6	gnd	ENET_SEL2	VACANT2A	gnd	n/c GTXD2[5]	gnd	gnd	gnd	gnd	gnd	J2	6
J2	5	gnd	ENET_SEL1	gnd	n/c GRXD2[6]	gnd	n/c GTXD2[6]	gnd	RXA[10]-	RXA[10]+	gnd	J2	5
J2	4	gnd	n/c ADDR, pullup	BD_PRES17AB	gnd	n/c GRXD2[5]	gnd	gnd	TXA[10]-	TXA[10]+	gnd	J2	4
J2	3	gnd	n/c ADDR, pullup	gnd	n/c GRXD2[4]	gnd	n/c GTXD2[7]	gnd	gnd	gnd	gnd	J2	3
J2	2	gnd	VDD +3.3	VDD I/O	gnd	n/c GRXD2[3]	gnd	gnd	RXA[11]-	RXA[11]+	gnd	J2	2
J2	1	gnd	VDD +3.3	gnd	n/c GRXD2[2]	gnd	n/c GTXD2[8]	gnd	TXA[11]-	TXA[11]+	gnd	J2	1
J1	11	gnd	VDD +3.3	VDD I/O	gnd	n/c GRXD2[1]	gnd	gnd	gnd	gnd	gnd	J1	11
J1	10	gnd	VDD +3.3	gnd EARTH	n/c GRXD2[0]	gnd	n/c GTXD2[9]	gnd	RXA[12]-	RXA[12]+	gnd	J1	10
J1	9	gnd	VDD +3.3	VDD I/O	gnd	n/c	gnd	gnd	TXA[12]-	TXA[12]+	gnd	J1	9
J1	8	gnd	VDD +3.3	gnd EARTH	n/c GRX_CLK2	gnd	n/c	gnd	gnd	gnd	gnd	J1	8
J1	7	gnd	VDD +3.3	VDD I/O	n/c GRX_CLKN2	n/c COM_DET2	n/c	gnd	RXA[13]-	RXA[13]+	gnd	J1	7
J1	6	gnd	VDD +5	gnd EARTH	gnd	gnd	gnd	gnd	TXA[13]-	TXA[13]+	gnd	J1	6
J1	5	gnd	VDD +5	VDD IPMB_PWR	n/c	n/c RES	n/c GTX_CLK2	gnd	gnd	gnd	gnd	J1	5
J1	4	gnd	VDD +5	gnd EARTH	n/c RES	n/c QoS	n/c RES	gnd	RXA[14]-	RXA[14]+	gnd	J1	4
J1	3	gnd	VDD +12	SMB SDA	SMB SAL	n/c RES	n/c RES	gnd	TXA[14]-	TXA[14]+	gnd	J1	3
J1	2	gnd	gnd	SMB WP	gnd	gnd	gnd	gnd	gnd	gnd	gnd	J1	2
J1	1	gnd	VDD -12	SMB SCL	gnd CONTACT#	RXA[15]-	RXA[15]+	gnd	TXA[15]-	TXA[15]+	gnd	J1	1
		Z	A	B	C	D	E	F	G	H	I		

FIGURE 7A

Bottom Front, 3U Fabric Board

J3	11	gnd	VDD -12	SMB SCL	gnd CONTACT#	RXB[0]+	RXB[0]-	gnd	TXB[0]-	TXB[0]+	gnd	J3	11
J3	10	gnd	gnd	SMB WP	gnd	gnd	gnd	gnd	gnd	gnd	gnd	J3	10
J3	9	gnd	VDD +12	SMB SDA	SMB SAL	n/c RES	n/c RES	gnd	TXB[1]+	TXB[1]-	gnd	J3	9
J3	8	gnd	VDD +5	gnd EARTH	n/c RES	n/c QoS	n/c RES	gnd	RXB[1]-	RXB[1]+	gnd	J3	8
J3	7	gnd	VDD +5	VDD IPMB_PWR	n/c GTX_CLK1	n/c RES	n/c	gnd	gnd	gnd	gnd	J3	7
J3	6	gnd	VDD +5	gnd EARTH	gnd	gnd	gnd	gnd	TXB[2]+	TXB[2]-	gnd	J3	6
J3	5	gnd	VDD +3.3	VDD I/O	n/c	n/c	n/c GRX_CLKN1	gnd	RXB[2]-	RXB[2]+	gnd	J3	5
J3	4	gnd	VDD +3.3	gnd EARTH	n/c	gnd	n/c GRX_CLK1	gnd	gnd	gnd	gnd	J3	4
J3	3	gnd	VDD +3.3	VDD I/O	gnd	n/c COM_DET1	gnd	gnd	TXB[3]+	TXB[3]-	gnd	J3	3
J3	2	gnd	VDD +3.3	gnd EARTH	n/c GTXD1[0]	gnd	n/c GRXD1[8]	gnd	RXB[3]-	RXB[3]+	gnd	J3	2
J3	1	gnd	VDD +3.3	VDD I/O	gnd	n/c GTXD1[1]	gnd	gnd	gnd	gnd	gnd	J3	1
J2	25	gnd	VDD +3.3	gnd	n/c GTXD1[2]	gnd	n/c GRXD1[9]	gnd	TXB[4]+	TXB[4]-	gnd	J2	25
J2	24	gnd	VDD +3.3	VDD I/O	gnd	n/c GTXD1[3]	gnd	gnd	RXB[4]-	RXB[4]+	gnd	J2	24
J2	23	gnd	n/c ADDR, pullup	gnd	n/c GTXD1[4]	gnd	n/c GRXD1[7]	gnd	gnd	gnd	gnd	J2	23
J2	22	gnd	gnd ADDR	BD_PRES17BF	gnd	n/c GTXD1[5]	gnd	gnd	TXB[5]+	TXB[5]-	gnd	J2	22
J2	21	gnd	ENET_SEL1	gnd	n/c GTXD1[6]	gnd	n/c GRXD1[6]	gnd	RXB[5]-	RXB[5]+	gnd	J2	21
J2	20	gnd	ENET_SEL2	VACANT1B	gnd	n/c GRXD1[5]	gnd	gnd	gnd	gnd	gnd	J2	20
J2	19	gnd	n/c RES	gnd	n/c GTXD1[7]	gnd	n/c GRXD1[4]	gnd	TXB[6]+	TXB[6]-	gnd	J2	19
J2	18	gnd	VDD +5	BD_SEL17BF	gnd	n/c GRXD1[3]	gnd	gnd	RXB[6]-	RXB[6]+	gnd	J2	18
J2	17	gnd	gnd	gnd	n/c GTXD1[9]	gnd	n/c GRXD1[2]	gnd	gnd	gnd	gnd	J2	17
J2	16	gnd	n/c TX[16]+	gnd	gnd	n/c GRXD1[1]	gnd	gnd	TXB[7]+	TXB[7]-	gnd	J2	16
J2	15	gnd	n/c TX[16]-	gnd	n/c GTXD1[8]	gnd	n/c GRXD1[0]	gnd	RXB[7]-	RXB[7]+	gnd	J2	15
J2	14	gnd	gnd	gnd	gnd	gnd	gnd	gnd	gnd	gnd	gnd	J2	14
J2	13	gnd	gnd	gnd	bottom front	gnd	gnd	gnd	gnd	gnd	gnd	J2	13
J2	12	gnd	gnd	gnd	gnd	gnd	gnd	gnd	gnd	gnd	gnd	J2	12
J2	11	gnd	n/c RX[16]-	gnd	n/c GRXD2[9]	gnd	n/c GTXD2[0]	gnd	RXB[8]-	RXB[8]+	gnd	J2	11
J2	10	gnd	n/c RX[16]+	gnd	gnd	n/c GTXD2[1]	gnd	gnd	TXB[8]+	TXB[8]-	gnd	J2	10
J2	9	gnd	gnd	gnd	n/c GRXD2[8]	gnd	n/c GTXD2[2]	gnd	gnd	gnd	gnd	J2	9
J2	8	gnd	VDD +5	BD_SEL17BB	gnd	n/c GTXD2[3]	gnd	gnd	RXB[9]-	RXB[9]+	gnd	J2	8
J2	7	gnd	n/c RES	gnd	n/c GRXD2[7]	gnd	n/c GTXD2[4]	gnd	TXB[9]+	TXB[9]-	gnd	J2	7
J2	6	gnd	ENET_SEL2	VACANT2B	gnd	n/c GTXD2[5]	gnd	gnd	gnd	gnd	gnd	J2	6
J2	5	gnd	ENET_SEL1	gnd	n/c GRXD2[6]	gnd	n/c GTXD2[6]	gnd	RXB[10]-	RXB[10]+	gnd	J2	5
J2	4	gnd	n/c ADDR, pullup	BD_PRES17BB	gnd	n/c GRXD2[5]	gnd	gnd	TXB[10]+	TXB[10]-	gnd	J2	4
J2	3	gnd	gnd ADDR	gnd	n/c GRXD2[4]	gnd	n/c GTXD2[7]	gnd	gnd	gnd	gnd	J2	3
J2	2	gnd	VDD +3.3	VDD I/O	gnd	n/c GRXD2[3]	gnd	gnd	RXB[11]-	RXB[11]+	gnd	J2	2
J2	1	gnd	VDD +3.3	gnd	n/c GRXD2[2]	gnd	n/c GTXD2[8]	gnd	TXB[11]+	TXB[11]-	gnd	J2	1
J1	11	gnd	VDD +3.3	VDD I/O	gnd	n/c GRXD2[1]	gnd	gnd	gnd	gnd	gnd	J1	11
J1	10	gnd	VDD +3.3	gnd EARTH	n/c GRXD2[0]	gnd	n/c GTXD2[9]	gnd	RXB[12]-	RXB[12]+	gnd	J1	10
J1	9	gnd	VDD +3.3	VDD I/O	gnd	n/c	gnd	gnd	TXB[12]+	TXB[12]-	gnd	J1	9
J1	8	gnd	VDD +3.3	gnd EARTH	n/c GRX_CLK2	gnd	n/c	gnd	gnd	gnd	gnd	J1	8
J1	7	gnd	VDD +3.3	VDD I/O	n/c GRX_CLKN2	n/c COM_DET2	n/c	gnd	RXB[13]-	RXB[13]+	gnd	J1	7
J1	6	gnd	VDD +5	gnd EARTH	gnd	gnd	gnd	gnd	TXB[13]+	TXB[13]-	gnd	J1	6
J1	5	gnd	VDD +5	VDD IPMB_PWR	n/c	n/c RES	n/c GTX_CLK2	gnd	gnd	gnd	gnd	J1	5
J1	4	gnd	VDD +5	gnd EARTH	n/c RES	n/c QoS	n/c RES	gnd	RXB[14]-	RXB[14]+	gnd	J1	4
J1	3	gnd	VDD +12	SMB SDA	SMB SAL	n/c RES	n/c RES	gnd	TXB[14]+	TXB[14]-	gnd	J1	3
J1	2	gnd	gnd	SMB WP	gnd	gnd	gnd	gnd	gnd	gnd	gnd	J1	2
J1	1	gnd	VDD -12	SMB SCL	gnd CONTACT#	RXB[15]+	RXB[15]-	gnd	TXB[15]-	TXB[15]+	gnd	J1	1
Z A B C D E F G H I													

FIGURE 7B

Top Rear, 3U Fabric Board

J1	1	gnd	VDD -12	SMB SCL	gnd CONTACT#	RXA[15]+	RXA[15]-	gnd	TXA[15]-	TXA[15]+	J3	#	gnd
J1	2	gnd	gnd	SMB WP	gnd	gnd	gnd	gnd	gnd	gnd	J3	11	gnd
J1	3	gnd	VDD +12	SMB SDA	SMB SAL	n/c RES	n/c RES	gnd	TXA[14]+	TXA[14]-	J3	#	gnd
J1	4	gnd	VDD +5	gnd EARTH	n/c RES	n/c QoS	n/c RES	gnd	RXA[14]-	RXA[14]+	J3	9	gnd
J1	5	gnd	VDD +5	VDD IPMB_PWR	n/c	n/c RES	n/c GTX_CLK2	gnd	gnd	gnd	J3	8	gnd
J1	6	gnd	VDD +5	gnd EARTH	gnd	gnd	gnd	gnd	TXA[13]+	TXA[13]-	J3	7	gnd
J1	7	gnd	VDD +3.3	VDD I/O	n/c GRX_CLKN2	n/c COM_DET2	n/c	gnd	RXA[13]-	RXA[13]+	J3	6	gnd
J1	8	gnd	VDD +3.3	gnd EARTH	n/c GRX_CLK2	gnd	n/c	gnd	gnd	gnd	J3	5	gnd
J1	9	gnd	VDD +3.3	VDD I/O	gnd	n/c	gnd	gnd	TXA[12]+	TXA[12]-	J3	4	gnd
J1	10	gnd	VDD +3.3	gnd EARTH	n/c GRXD2[0]	gnd	n/c GTXD2[9]	gnd	RXA[12]-	RXA[12]+	J3	3	gnd
J1	11	gnd	VDD +3.3	VDD I/O	gnd	n/c GRXD2[1]	gnd	gnd	gnd	gnd	J3	2	gnd
J2	1	gnd	VDD +3.3	gnd	n/c GRXD2[2]	gnd	n/c GTXD2[8]	gnd	TXA[11]+	TXA[11]-	J2	#	gnd
J2	2	gnd	VDD +3.3	VDD I/O	gnd	n/c GRXD2[3]	gnd	gnd	RXA[11]-	RXA[11]+	J2	#	gnd
J2	3	gnd	n/c ADDR, pullup	gnd	n/c GRXD2[4]	gnd	n/c GTXD2[7]	gnd	gnd	gnd	J2	#	gnd
J2	4	gnd	n/c ADDR, pullup	BD_PRES17AB	gnd	n/c GRXD2[5]	gnd	gnd	TXA[10]+	TXA[10]-	J2	#	gnd
J2	5	gnd	ENET_SEL1	gnd	n/c GRXD2[6]	gnd	n/c GTXD2[6]	gnd	RXA[10]-	RXA[10]+	J2	#	gnd
J2	6	gnd	ENET_SEL2	VACANT2A	gnd	n/c GTXD2[5]	gnd	gnd	gnd	gnd	J2	#	gnd
J2	7	gnd	n/c RES	gnd	n/c GRXD2[7]	gnd	n/c GTXD2[4]	gnd	TXA[9]+	TXA[9]-	J2	#	gnd
J2	8	gnd	VDD +5	BD_SEL17AB	gnd	n/c GTXD2[3]	gnd	gnd	RXA[9]-	RXA[9]+	J2	#	gnd
J2	9	gnd	gnd	gnd	n/c GRXD2[8]	gnd	n/c GTXD2[2]	gnd	gnd	gnd	J2	#	gnd
J2	10	gnd	n/c RX[16]+	gnd	gnd	n/c GTXD2[1]	gnd	gnd	TXA[8]+	TXA[8]-	J2	17	gnd
J2	11	gnd	n/c RX[16]-	gnd	n/c GRXD2[9]	gnd	n/c GTXD2[0]	gnd	RXA[8]-	RXA[8]+	J2	#	gnd
J2	12	gnd	gnd	gnd	gnd	gnd	gnd	gnd	gnd	gnd	J2	15	gnd
J2	13	gnd	gnd	gnd	gnd	gnd	gnd	gnd	gnd	gnd	J2	#	gnd
J2	14	gnd	gnd	gnd	gnd	gnd	gnd	gnd	gnd	gnd	J2	#	gnd
J2	15	gnd	n/c TX[16]-	gnd	n/c GTXD1[8]	gnd	n/c GRXD1[0]	gnd	RXA[7]-	RXA[7]+	J2	#	gnd
J2	16	gnd	n/c TX[16]+	gnd	gnd	n/c GRXD1[1]	gnd	gnd	TXA[7]+	TXA[7]-	J2	11	gnd
J2	17	gnd	gnd	gnd	n/c GTXD1[9]	gnd	n/c GRXD1[2]	gnd	gnd	gnd	J2	#	gnd
J2	18	gnd	VDD +5	BD_SEL17AF	gnd	n/c GRXD1[3]	gnd	gnd	RXA[6]-	RXA[6]+	J2	9	gnd
J2	19	gnd	n/c RES	gnd	n/c GTXD1[7]	gnd	n/c GRXD1[4]	gnd	TXA[6]+	TXA[6]-	J2	8	gnd
J2	20	gnd	ENET_SEL2	VACANT1A	gnd	n/c GRXD1[5]	gnd	gnd	gnd	gnd	J2	7	gnd
J2	21	gnd	ENET_SEL1	gnd	n/c GTXD1[6]	gnd	n/c GRXD1[6]	gnd	RXA[5]-	RXA[5]+	J2	6	gnd
J2	22	gnd	gnd ADDR	BD_PRES17AF	gnd	n/c GTXD1[5]	gnd	gnd	TXA[5]+	TXA[5]-	J2	5	gnd
J2	23	gnd	gnd ADDR	gnd	n/c GTXD1[4]	gnd	n/c GRXD1[7]	gnd	gnd	gnd	J2	4	gnd
J2	24	gnd	VDD +3.3	VDD I/O	gnd	n/c GTXD1[3]	gnd	gnd	RXA[4]-	RXA[4]+	J2	3	gnd
J2	25	gnd	VDD +3.3	gnd	n/c GTXD1[2]	gnd	n/c GRXD1[9]	gnd	TXA[4]+	TXA[4]-	J2	2	gnd
J3	1	gnd	VDD +3.3	VDD I/O	gnd	n/c GTXD1[1]	gnd	gnd	gnd	gnd	J1	#	gnd
J3	2	gnd	VDD +3.3	gnd EARTH	n/c GTXD1[0]	gnd	n/c GRXD1[8]	gnd	RXA[3]-	RXA[3]+	J1	11	gnd
J3	3	gnd	VDD +3.3	VDD I/O	gnd	n/c COM_DET1	gnd	gnd	TXA[3]+	TXA[3]-	J1	#	gnd
J3	4	gnd	VDD +3.3	gnd EARTH	n/c	gnd	n/c GRX_CLK1	gnd	gnd	gnd	J1	9	gnd
J3	5	gnd	VDD +3.3	VDD I/O	n/c	n/c	n/c GRX_CLKN1	gnd	RXA[2]-	RXA[2]+	J1	8	gnd
J3	6	gnd	VDD +5	gnd EARTH	gnd	gnd	gnd	gnd	TXA[2]+	TXA[2]-	J1	7	gnd
J3	7	gnd	VDD +5	VDD IPMB_PWR	n/c GTX_CLK1	n/c RES	n/c	gnd	gnd	gnd	J1	6	gnd
J3	8	gnd	VDD +5	gnd EARTH	n/c RES	n/c QoS	n/c RES	gnd	RXA[1]-	RXA[1]+	J1	5	gnd
J3	9	gnd	VDD +12	SMB SDA	SMB SAL	n/c RES	n/c RES	gnd	TXA[1]+	TXA[1]-	J1	4	gnd
J3	10	gnd	gnd	SMB WP	gnd	gnd	gnd	gnd	gnd	gnd	J1	3	gnd
J3	11	gnd	VDD -12	SMB SCL	gnd CONTACT#	RXA[0]+	RXA[0]-	gnd	TXA[0]-	TXA[0]+	J1	2	gnd
Z A B C D E F G H Z													

Figure 7C

Bottom Rear, 3U Fabric Board

J1	1	gnd	VDD -12	SMB SCL	gnd CONTACT#	RXB[15]+	RXB[15]-	gnd	TXB[15]-	TXB[15]+	J3	#	gnd
J1	2	gnd	gnd	SMB WP	gnd	gnd	gnd	gnd	gnd	gnd	J3	11	gnd
J1	3	gnd	VDD +12	SMB SDA	SMB SAL	n/c RES	n/c RES	gnd	TXB[14]+	TXB[14]-	J3	#	gnd
J1	4	gnd	VDD +5	gnd EARTH	n/c RES	n/c CoS	n/c RES	gnd	RXB[14]-	RXB[14]+	J3	9	gnd
J1	5	gnd	VDD +5	VDD IPMB_PWR	n/c	n/c RES	n/c GTX_CLK2	gnd	gnd	gnd	J3	8	gnd
J1	6	gnd	VDD +5	gnd EARTH	gnd	gnd	gnd	gnd	TXB[13]+	TXB[13]-	J3	7	gnd
J1	7	gnd	VDD +3.3	VDD I/O	n/c GRX_CLKN2	n/c COM_DET2	n/c	gnd	RXB[13]-	RXB[13]+	J3	6	gnd
J1	8	gnd	VDD +3.3	gnd EARTH	n/c GRX_CLK2	gnd	n/c	gnd	gnd	gnd	J3	5	gnd
J1	9	gnd	VDD +3.3	VDD I/O	gnd	n/c	gnd	gnd	TXB[12]+	TXB[12]-	J3	4	gnd
J1	10	gnd	VDD +3.3	gnd EARTH	n/c GRXD2[0]	gnd	n/c GTXD2[9]	gnd	RXB[12]-	RXB[12]+	J3	3	gnd
J1	11	gnd	VDD +3.3	VDD I/O	gnd	n/c GRXD2[1]	gnd	gnd	gnd	gnd	J3	2	gnd
J2	1	gnd	VDD +3.3	gnd	n/c GRXD2[2]	gnd	n/c GTXD2[8]	gnd	TXB[11]+	TXB[11]-	J2	#	gnd
J2	2	gnd	VDD +3.3	VDD I/O	gnd	n/c GRXD2[3]	gnd	gnd	RXB[11]-	RXB[11]+	J2	#	gnd
J2	3	gnd	gnd ADDR	gnd	n/c GRXD2[4]	gnd	n/c GTXD2[7]	gnd	gnd	gnd	J2	#	gnd
J2	4	gnd	n/c ADDR, pullup	BD_PRES178B	gnd	n/c GRXD2[5]	gnd	gnd	TXB[10]+	TXB[10]-	J2	#	gnd
J2	5	gnd	ENET_SEL1	gnd	n/c GRXD2[6]	gnd	n/c GTXD2[6]	gnd	RXB[10]-	RXB[10]+	J2	#	gnd
J2	6	gnd	ENET_SEL2	VACANT2B	gnd	n/c GTXD2[5]	gnd	gnd	gnd	gnd	J2	#	gnd
J2	7	gnd	n/c RES	gnd	n/c GRXD2[7]	gnd	n/c GTXD2[4]	gnd	TXB[9]+	TXB[9]-	J2	#	gnd
J2	8	gnd	VDD +5	BD_SEL178B	gnd	n/c GTXD2[3]	gnd	gnd	RXB[9]-	RXB[9]+	J2	#	gnd
J2	9	gnd	gnd	gnd	n/c GRXD2[8]	gnd	n/c GTXD2[2]	gnd	gnd	gnd	J2	#	gnd
J2	10	gnd	n/c RX[16]+	gnd	gnd	n/c GTXD2[1]	gnd	gnd	TXB[8]+	TXB[8]-	J2	17	gnd
J2	11	gnd	n/c RX[16]-	gnd	n/c GRXD2[9]	gnd	n/c GTXD2[0]	gnd	RXB[8]-	RXB[8]+	J2	#	gnd
J2	12	key	key	key	key	key	key	key	key	key	J2	15	key
J2	13	key	key	key	key	key	key	key	key	key	J2	#	key
J2	14	key	key	key	key	key	key	key	key	key	J2	#	key
J2	15	gnd	n/c TX[16]-	gnd	n/c GTXD1[8]	gnd	n/c GRXD1[0]	gnd	RXB[7]-	RXB[7]+	J2	#	gnd
J2	16	gnd	n/c TX[16]+	gnd	gnd	n/c GRXD1[1]	gnd	gnd	TXB[7]+	TXB[7]-	J2	11	gnd
J2	17	gnd	gnd	gnd	n/c GTXD1[9]	gnd	n/c GRXD1[2]	gnd	gnd	gnd	J2	#	gnd
J2	18	gnd	VDD +5	BD_SEL178F	gnd	n/c GRXD1[3]	gnd	gnd	RXB[6]-	RXB[6]+	J2	9	gnd
J2	19	gnd	n/c RES	gnd	n/c GTXD1[7]	gnd	n/c GRXD1[4]	gnd	TXB[6]+	TXB[6]-	J2	8	gnd
J2	20	gnd	ENET_SEL2	VACANT1B	gnd	n/c GRXD1[5]	gnd	gnd	gnd	gnd	J2	7	gnd
J2	21	gnd	ENET_SEL1	gnd	n/c GTXD1[6]	gnd	n/c GRXD1[6]	gnd	RXB[5]-	RXB[5]+	J2	6	gnd
J2	22	gnd	gnd ADDR	BD_PRES178F	gnd	n/c GTXD1[5]	gnd	gnd	TXB[5]+	TXB[5]-	J2	5	gnd
J2	23	gnd	n/c ADDR, pullup	gnd	n/c GTXD1[4]	gnd	n/c GRXD1[7]	gnd	gnd	gnd	J2	4	gnd
J2	24	gnd	VDD +3.3	VDD I/O	gnd	n/c GTXD1[3]	gnd	gnd	RXB[4]-	RXB[4]+	J2	3	gnd
J2	25	gnd	VDD +3.3	gnd	n/c GTXD1[2]	gnd	n/c GRXD1[9]	gnd	TXB[4]+	TXB[4]-	J2	2	gnd
J3	1	gnd	VDD +3.3	VDD I/O	gnd	n/c GTXD1[1]	gnd	gnd	gnd	gnd	J1	#	gnd
J3	2	gnd	VDD +3.3	gnd EARTH	n/c GTXD1[0]	gnd	n/c GRXD1[8]	gnd	RXB[3]-	RXB[3]+	J1	11	gnd
J3	3	gnd	VDD +3.3	VDD I/O	gnd	n/c COM_DET1	gnd	gnd	TXB[3]+	TXB[3]-	J1	#	gnd
J3	4	gnd	VDD +3.3	gnd EARTH	n/c	gnd	n/c GRX_CLK1	gnd	gnd	gnd	J1	9	gnd
J3	5	gnd	VDD +3.3	VDD I/O	n/c	n/c	n/c GRX_CLKN1	gnd	RXB[2]-	RXB[2]+	J1	8	gnd
J3	6	gnd	VDD +5	gnd EARTH	gnd	gnd	gnd	gnd	TXB[2]+	TXB[2]-	J1	7	gnd
J3	7	gnd	VDD +5	VDD IPMB_PWR	n/c GTX_CLK1	n/c RES	n/c	gnd	gnd	gnd	J1	6	gnd
J3	8	gnd	VDD +5	gnd EARTH	n/c RES	n/c CoS	n/c RES	gnd	RXB[1]-	RXB[1]+	J1	5	gnd
J3	9	gnd	VDD +12	SMB SDA	SMB SAL	n/c RES	n/c RES	gnd	TXB[1]+	TXB[1]-	J1	4	gnd
J3	10	gnd	gnd	SMB WP	gnd	gnd	gnd	gnd	gnd	gnd	J1	3	gnd
J3	11	gnd	VDD -12	SMB SCL	gnd CONTACT#	RXB[0]+	RXB[0]-	gnd	TXB[0]-	TXB[0]+	J1	2	gnd
Z		A	B	C	D	E	F	G	H	Z			

Figure 7D